Cable Wiring Diagram for WXTrak in Parallel with KPC-3+ to Radio Transceiver

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Jumper settings in KPC-3+:
    J6 = center to 2: Puts power on KPC-3+ radio port connector to feed to WXTrak
    J7 = center to 2: Puts power on KPC-3+ radio port connector to feed to WXTrak
                  : Insures PTT output is truly an open drain output.
Software settings in KPC-3+:
    CD = Software
                 : Allows KPC-3+ to hold off transmit when XCD is present
KPC-3+ Radio Port
                       WXTrak Radio Port
                                                       Radio Connector
    (DB-9M)
                            (DB-9M)
                                                           (DB-9F)
                                         Brown 1 Transmit Audio (MIC)
1 TXA Brown
                      1 TXA ___
            Black
2 XCD ____
                       2 XCD
                                                   2 (N.C.)
                                 1N4148
                                         White
                       3 PTT -
                                                   3 PTT
3 PTT -
           White
                       4 (N.C.)
4 (N.C.)
                                                   4 (N.C.)
                                        5 RXA
           Green
5 RXA
                                        Shield 6 GND
           Shield 6 GND _____
            Red
                                         Red
7 PWR —
                     - 7 PWR -
                                               — 7 PWR (from Radio if available;
                                                          otherwise, N.C. at radio)
8 (N.C.)
                       8 (N.C.)
                                                   8 (N.C.)
9 (N.C.)
                       9 (N.C.)
                                                   9 (N.C.)
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Note: It would be nice to use the WxTrak's XCD input pin to hold off its transmission when the KPC3+ is transmitting (KPC3+'s PTT being active). However, the implementation of the WxTrak's XCD input results in a current path being present through its XCD input pin. This prevents one from using the function unless additional buffering/logic is put into the path.